REMARKS

This amendment responds to the office action mailed September 19, 2005 . In the office action the Examiner:

- allowed claims 59-66;

- rejected claims 32-34, 36-38, 40-45, 48-50, 52-56, 67, 68 and 70-72 under 35 U.S.C. 102(e) as anticipated by Barth et al. (US 6,154,821); and

- objected to claims 35, 39, 46, 47, 51, 57, 58, 69, and 73 as being dependent upon a rejected base claim.

After entry of this amendment, the pending claims are: claims 32-73

REMARKS CONCERNING REVISED FIGURES

Revised Figures 1-10 are included in Appendix A. The revised figures include the designation "Prior Art" in the legend as required by the Examiner in the present Office Action.

CLAIM OBJECTION

Claim 58 has been amended to correct the informality identified by the Examiner in the present Office Action.

Claims 44 and 66 have been amended to correct a typographical error.

35 U.S.C. 102(e) REJECTION

Claims 32-34, 36-38, 40-45, 48-50, 52-56, 67, 68 and 70-72 were rejected as anticipated by Barth et al. The Applicants respectfully disagree and traverse.

For example, the present office action states that Barth et al. ("Barth") discloses, among other things, "a first register to store a first value representative of a sampling time adjustment (150) that is applied to the sample time..." (see page 3, present office action). The Applicants submit that Barth does not disclose a first register to store a first value representative of a sampling time adjustment as is required by claim 32. Instead, the so-called "first offset circuit 150" disclosed in Barth is configured to determine certain clock cycle offsets communicated to DRAM devices, for example, "the first offset circuit (150) is configured to determine a minimum clock cycle offset between a read command and a subsequent write command communicated to the DRAM devices 110-116 ..." (col. 5, lines

34-37). Therefore, for at least this reason, Barth et al. does not anticipate claim 32 and the rejected claims that depend from claim 32.

Barth does not disclose determining a first value that represents a sample time adjustment to apply to a sample time of a receiver circuit as is required by claim 48. Therefore, for at least this reason, Barth et al. does not anticipate claim 48 and the rejected claims that depend from claim 48.

Barth does not disclose "a first register to store a first value representative of a sampling time adjustment that is applied to the sample time" used by a receiver circuit, as is required by claim 67. Therefore, for at least this reason, Barth et al. does not anticipate claim 67 and the rejected claims that depend from claim 67.

Barth does not disclose "a means for storing a first value representative of a sampling time adjustment that is applied to the sample time" used by a sampling receive means, as is required by claim 71. Therefore, for at least this reason, Barth et al. does not anticipate claim 71 and the rejected claims that depend from claim 71.
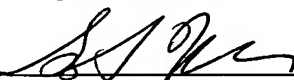
Removal of this ground for rejection is respectfully requested.

CONCLUSION

In light of the above amendments and remarks, the Applicant respectfully requests that the Examiner reconsider this application with a view towards allowance. The Examiner is invited to call the undersigned attorney at (650) 843-4000, if a telephone call could help resolve any remaining items.

Respectfully submitted,

Date: October 6, 2005   31,066
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